



*Priyadarshini Engineering College,
Vaniyambadi-635751.*

*Department of Electrical and Electronics
Engineering*

EDITORIAL BOARD

PATRON: *Dr.P.NATARAJAN,M.E.,Ph.D.*

CO-PATRON: *Mrs.N.JAYANTHI,M.TECH.,(Ph.D)*

FACULTY INCHARGE :

ASST.PROF.MR.S.NIRMAL RAJAN

ASST .PROF.MR.K.SATHISH KUMAR,M.E.,

CHIEF EDITOR

ASRHAD MASHOOD.S(Final year)

MARI.P (Final year)

MD.TAHIR.M.A (Final year)

**MYSTERIOUS
TRANSIENTS**

WHERE POWER BREATH

ABOUT THE INSTITUTE

Priyadarshini Engineering College, the flagship of Jai Barath Charitable Trust, was established in 1995 at Vaniyambadi in Vellore District of Tamil Nadu. The college has been approved by All India Council for Technical Education, New Delhi & affiliated to Ana University, Chennai. Priyadarshini Engineering College situated in the rural area of Vaniyambadi, Vellore District is committed to the vision of developing itself into a multi-campus, Inter-disciplinary Institution of Excellence through symbiotic efforts and innovative practices of management and faculty to provide the student with an ambient academic environment, ideal for the pursuit of knowledge and development carrier.

VISION OF THE INSTITUTE

- *To inculcate in the young rural minds the aptitude to compete with the quality technocrats.*

MISSION OF THE INSTITUTE

- *To instill technical skills to compete in the sustainable world*
- *To impart holistic value based technical education*
- *To intensify research and development (r & d) activities in technological development*
- *To imbibe core values of love for motherland performance of duty, compassion, tolerance, honesty and integrity*

MOTTO

perseverance, endurance, commitment

“கற்றலும், கற்றவை கேட்டலும், கேட்டதன்கண் நிற்கலும்”

HOD MESSAGE



As the Head of the Department of EEE, I am extremely happy to highlight the progress made by the Department of Electrical and Electronics Engineering at Priyadarshini Engineering College. I am delighted to inform you that the department at present has 13 highly talented faculty members of which 1 are Doctorates and 6 Faculty members are pursuing their Ph.D. We have all laboratories with full facility required for EEE curriculum .We have well equipped department library to cater to the needs of standard text books for all EEE subjects for the benefit of our staff and students Our academic programme aims to prepare the students for new challenges in the area of Electrical and Electronics Engineering. We are committed to give our students an environment where they would be able to develop critical thinking and problem-solving skills as they advance through the program. In addition to class room teaching, the students are guided and motivated to practically implement the principles learnt in classroom through experimentation in the laboratories which help them gain confidence and become skilled engineering professionals. Whether you are a student, parent, prospective faculty member or a curious individual, we invite you to visit our campus to witness the state-of-the-art facilities and excellent teaching-learning atmosphere. Our enthusiasm knows no bounds even in nascent stages, and our dedication to lead Electrical and Electronics Engineering to new heights is indeed strong. We look forward to hearing from you.

VISION OF THE DEPARTMENT

- To produce eminent electrical engineer specifically from the rural background.

MISSION OF THE DEPARTMENT

- Infuse moral ethics and good virtues to the students
- Providing good technical knowledge for innovative research and development
- Making them excellent in extracurricular activities

PROGRAMME EDUCATIONAL OBJECTIVES (PEOS)

PEO 1: Core Competence

Graduates excel in analyzing, designing, simulating and testing of all electrical and electronics systems.

PEO2: Societal Requirements

Graduates are successful in giving solutions for real time problems to cater to the industrial and societal requirements.

PEO 3: Lifelong Learning

Graduates can adapt to lifelong learning to enhance their technical skills.

PEO 4: Leadership Qualities

Graduates exhibit their leadership qualities in a multidisciplinary field.

PROGRAMME OUTCOMES(POS)

PO1: Engineering Knowledge:

Understand and apply basic concepts of Mathematics, Physics, Chemistry and Engineering.

PO2: Problem Analysis :

Understand and analyze circuit theory, electromagnetic theory, control theory and apply them to electrical engineering applications.

PO3: Design &Development of Solutions:

Analyze and design the electrical and electronics components and to apply in solid state drives and power systems.

PO4: Investigation of Complex problems:

Conduct investigation in complex problems of power system operation, stability, control and protection.

PO5: Modern Tool Usage:

Use contemporary computing tools and techniques in Electrical Engineering applications.

PO6: Engineer and Society:

Handle engineering aspects of electrical energy, utilization and the impact of engineering solutions to the societal needs.

PO7: Environment & Sustainability:

Acquire knowledge of contemporary issues to sustain the ever changing environment.

PO8: Ethics:

Apply the ethical principles to their profession and social issues.

PO9: Individual & Team Work:

Perform individually and in a group to accomplish a common goal.

PO10: Communication:

Effectively communicate and present technological developments.

PO11: Lifelong Learning:

Gain self- confidence to engage in lifelong learning.

PO12: Project Management & Finance:

Plan and manage a project in a cost effective manner.

IEEE STUDENTS CHAPTER

Department inaugurated IEEE Students Chapter on August 1st 2015 at Dr.A.P.J ABDUL KALAM AUDITORIUM by 10 A.M . The Function was presided over by the Principal Dr.P.Natarajan and Vice Principal felicitated the Chief Guest.



The Department Conducted IEEE Awareness Program on 7th September 2015. Mr.Prasanth Mohan Young professional Society Chair,IEEE ,Madras Section was the chief guest.



A Technical Seminar on "Digital India, Make in India, Skill India, Consumer Electronics Product Safety and Recent Innovations in Nanotechnology" was organized by IEEE Computer Society and IEEE Product Safety Engineering Society (PSES), Madras Chapter on 28.10.2015 at 10.30 am at Priyadarshini Engineering College (PEC), Vaniyambadi, Vellore. Presidential address was delivered by Dr.P.Natarajan, Principal. Chief Guest Mr.H.R.Mohan Chairman, IEEE Computer society, Madras Chapter, Chennai, presented a session on Digital India, Make in India and Skill India.

IEEE Computer Society and IEEE Product Safety Engineering Society Sponsored Technical Seminar on "Digital India, Make in India and skill India, Consumer Electronics product safety and Recent Innovations in Nanotechnology"

During his presentation Mr.H.R.Mohan explained the impact of Digital India, Nine pillars of Digital India, Make in India and its importance etc...and presented about Skill India and its advantages represented by our Prime Minister Shri. NarendraModi and initiatives.

Dr.R.K.Vim alnanthan, Treasurer, IEEE-PSES Madras Chapter presented a session on Consumer Product safety and our IEEE Faculty advisor Dr.R.Thilepa presented a session on Recent Innovations inNanotechnology. Dr.V.Jayaprakasan, Chairman, IEEE Product Safety Engineering Society Presented about IEEE Awareness to the students and faculty members.

During his presentation Dr.R.K.VimalNandhan covered various techniques of energy saving by renewable energy in solar systems such as solar photovoltaic cells, energy storage systems, Solar Power Costs, efficiency of various designs and challenges with regard to sustainability, environmental concerns.

Presentation Session on “Digital India, Make in India and Skill India” by Mr.H.R.Mohan



Presentation Session on “Consumer Product Safety” by Dr.R.K.VimalNandhan

Presentation Session on “Recent Innovations in Nanotechnology”



Presentation Session on “IEEE Awareness and Benefits” by Dr.V.Jayaprakasan

“PSPICE SIMULATION FOR ELECTRICAL ENGINEERS” on 21st September 2015.



Department organized IEEE Sponsored One Day Guest Lecture on “RECENT TRENDS IN IT” on 14th December 2015.

STAFF ACHIEVEMENTS

S.No.	NAME OF THE FACUTY	TITLE	JOURNAL
1	Mrs.N.Jayanthi	Power Flow Control by using Hybrid Power Generation	International Research Journal in Advanced Engineering and Technology Vol. 1, Issue4 pp201-206
2	Mrs.C.Bhuvanewari	DC Component minimization in transformer less three phase grid connected photo voltaic inverters by Integral method	International Journal of Innovative Research in Science Engineering and Technology Vol. 4, Issue12 ISSN2319-8753
3	Mrs.R.Rajeswari	Grid side converter control of Doubly Fed Induction Generation for Wind energy conversion	International Research Journal in Advanced Engineering and Technology Vol. 1, Issue4 pp320-327
		An Improved IUPQC controller to provide additional grid voltage	International Research Journal in Advanced Engineering and Technology Vol. 1, Issue4 pp275-283
4	Mrs.K.Malarkodi	DC to DC Converter for OFF shore wind energy generation by using Resonant switched capacitor cell	International Research Journal in Advanced Engineering and Technology Vol. 1, Issue4 pp268-274
5	Mr.C.Kalaiarasan	An Underfrequency load shedding scheme for Hybrid Power Systems	International Journal of Scientific Engineering and Technology Research Vol. 4 Issue 52, ISSN 2319-8885 pp11251-11260
6	Mr.S.Nirmal Rajan	Damping of sub synchronous resonance using neural network controlled Unified Power Flow Controller	International Journal of Engineering and Techniques Vol. 1 Issue 5 ISSN2395-1303
7	Mr.K.Sathish Kumar	Transient Stability Analysis in Microgrid	International Research Journal in Advanced Engineering and Technology Vol. 1, Issue4 pp259-267
		Co-Ordination of Directional Over current relay for Distributed System	International Journal of Innovative Research in Science Engineering and Technology Vol. 4, Issue12 ISSN2319-8753
8	Mr.A.Ashok Kumar	A Versatile control scheme of a dynamic voltage restorer utracapacitor for power quality improvement in grid	International Research Journal in Advanced Engineering and Technology Vol. 1, Issue4 pp328-335

		Power flow control of multi terminal DC network faults under unbalanced grid condition	International Research Journal in Advanced Engineering and Technology Vol. 1, Issue4 pp292-297
9	Mr.P.Elumalai	Power Flow Control by using Hybrid Power Generation	International Research Journal in Advanced Engineering and Technology Vol. 1, Issue4 pp201-206

STUDENTS ACHEIVEMENTS:

S.No.	EVENT NAME	COLLEGE	PARTICIPANT	DATE
1.	Electroforum 2015- Technical Quiz	P.S.V College of Engineering & Technology	ArshadMasuood.P	16-09-2015
2.	Electroforum 2015- Non Technical	P.S.V College of Engineering & Technology	ArshadMasuood.P	16-09-2015
3.	Electroforum 2015- Non Technical Quiz	P.S.V College of Engineering & Technology	Mohammed Tahir M.A	16-09-2015
4.	Electroforum 2015- Technical Quiz	P.S.V College of Engineering & Technology	Mohammed Tahir M.A	16-09-2015
5.	Electroforum 2015- Non Technical Quiz	P.S.V College of Engineering & Technology	Azharviqar .V.A	16-09-2015
6.	Electroforum 2015- Technical Quiz	P.S.V College of Engineering & Technology	Azharviqar .V.A	16-09-2015
7.	Electroforum 2015- Technical Quiz	P.S.V College of Engineering & Technology	Mohammed Tahir.V	16-09-2015
8.	Electroforum 2015- Non Technical Quiz	P.S.V College of Engineering & Technology	Mohammed Tahir.V	16-09-2015