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PRIYADARSHINI ENGINEERING COLLEGE
VANIYAMBADI 635751

DEPARTMENT OF ELECTRICAL AND ELECTRONICS
ENGINEERING

MYSTERIOUS
TRANSIENTS

DECEMBER 2016

THE
NEWS LETTER
OF
EEE DEPARTMENT

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ABOUT THE INSTITUTE

Priyadarshini Engineering College, the flagship of Jai Barath Charitable Trust, was established in 1995 at Vaniyambadi in Vellore District of Tamil Nadu. The college has been approved by All India Council for Technical Education, New Delhi & affiliated to Anna University, Chennai. Priyadarshini Engineering College situated in the rural area of Vaniyambadi, Vellore District is committed to the vision of developing itself into a multi-campus, Inter-disciplinary Institution of Excellence through symbiotic efforts and innovative practices of management and faculty to provide the student with an ambient academic environment, ideal for the pursuit of knowledge and

VISION OF THE INSTITUTE

To inculcate in the young rural minds the aptitude to compete with the quality technocrats.

MISSION OF THE INSTITUTE

- *To instill technical skills to compete in the sustainable world*
- *To impart holistic value based technical education*
- *To intensify research and development (r & d) activities in technological development*
- *To imbibe core values of love for motherland*

MOTTO

**Perseverance, Endurance,
Commitment**

“கற்றலும், கற்றவைகேட்டலும், கேட்டதன்கண்நிற்றலும்

HOD MESSAGE

The Department of EEE is focused on devising innovative, accurate solutions to complex problems, whilst ensuring that the solutions execute optimally. the strength of our management, researchers, faculties, alumnus and students also plays the vital role in providing quality education. We continue to play a leading role in our discipline and are committed towards creating innovative and effective professional graduate community which would vivacious and provide continuous learning .



VISION OF THE DEPARTMENT

To produce eminent electrical engineer specifically from the rural background.

MISSION OF THE DEPARTMENT

- *Infuse moral ethics and good virtues to the students*
- *Providing good technical knowledge for innovative research and development*
- *Making them excellent in extracurricular activities*

Programme Educational Objectives (PEOs)

PEO 1: Core Competence

Graduates excel in analyzing, designing, simulating and testing of all electrical and electronics systems.

PEO2: Societal Requirements

Graduates are successful in giving solutions for real time problems to cater to the industrial and societal requirements.

PEO 3: Lifelong Learning

Graduates can adapt to lifelong learning to enhance their technical skills.

PEO 4: Leadership Qualities

Graduates exhibit their leadership qualities in a multidisciplinary field.

Programme Outcomes (POs)

PO1: Engineering Knowledge:

Understand and apply basic concepts of Mathematics, Physics, Chemistry and Engineering.

PO2: Problem Analysis :

Understand and analyze circuit theory, electromagnetic theory, control theory and apply them to electrical engineering applications.

PO3: Design & Development of Solutions:

Analyze and design the electrical and electronics components and to apply in solid state drives and power systems.

PO4: Investigation of Complex problems:

Conduct investigation in complex problems of power system operation, stability, control and protection.

PO5: Modern Tool Usage:

Use contemporary computing tools and techniques in Electrical Engineering applications.

PO6: Engineer and Society:

Handle engineering aspects of electrical energy, utilization and the impact of engineering solutions to the societal needs.

PO7: Environment & Sustainability:

Acquire knowledge of contemporary issues to sustain the ever changing environment.

PO8: Ethics:

Apply the ethical principles to their profession and social issues.

PO9: Individual & Team Work:

Perform individually and in a group to accomplish a common goal.

PO10:

Communication:

Effectively communicate and present technological developments.

PO11: Lifelong Learning:

Gain self-confidence to engage in lifelong learning.

PO12: Project Management & Finance:

Plan and manage a project in a cost effective manner.

CONFERENCE /WORKSHOP/SEMINAR ATTENDED BY THE STAFFS:

- Mrs.R.Rajeswari, Associate Professor has presented a paper entitled "Comparison Of PSO Models For Optical Placement Of STATCOM" in ICRATED'16 on 27th & 28th July -2016 at C.Abdul Hakeem College of Engineering and Technology, Melvisharam, Vellore, Tamilnadu, India.
- Mrs.C.Bhuvanewari, Associate Professor has participated in the "National Workshop on Challenges and opportunities in Solar Energy Collection , Storage and Utilization" held between 29th and 30th July 2016 at VIT University , Vellore, Tamilnadu , India.
- Mrs.N.Jayanthi, Associate Professor has participated in the "National Workshop on Challenges and opportunities in Solar Energy Collection , Storage and Utilization" held between 29th and 30th July 2016 at VIT University , Vellore, Tamilnadu , India.
- Mrs.R.Thilepa,Prof.,Mrs.N.JayanthiAsso.Prof,
Mrs.C.BhuvanewariAsso.Prof,
Mrs.R.RajeswariAsso.Prof,
Mr.M.SelvaperumalAsso.Prof,
Mrs.K.MalarkodiAsst.Prof, Mr.V.VijiAsst.Prof,
Mr.P.ElumalaiAsst.Prof has participated in the One day Workshop on " Writing Research Papers, Research Proposals & Awareness on Funding Agencies and their Funding Mechanisms" held at Priyadarshini Engineering College, Vaniyambadi-635751 on 11th November 2016.

**STUDENTS
ACHIEVEMENTS**

1.S.AswinRaj and V.Thiyagaraj has participated in an Paper Presentation **ROBOTICS** and Secured **THIRD PLACE** at "SPEED 16 A NATIONAL LEVEL TECHNICAL SYMPOSIUM" held on 3rd September 2016.

I.S.Aswin Raj and V.Thiyagarajof has participated in an Non-Technical Event "SURPRISING" and Secured **FIRST** place at "SPEED 16 A NATIONAL LEVEL TECHNICAL SYMPOSIUM" held on 3rd September 2016.

B.Limba ,PG Student published a International Journal of Advanced Engineering and Research Development (IJAERD) entitled "Voltage Control and Stability Analysis for an Autonomous AC Microgrid " Paper ID #: IJAERD2016V03I0938373 , Volume 03 Issue 09, September - 2016

Mr. M.Sankar Ganesh ,PG Student published a International Journal of Advanced Engineering and Research Development (IJAERD) entitled "Three Phase 19 Level Modular Multilevel Inverter "Paper ID #: IJAERD2016V03I1151266 , Volume 03 Issue 11. November - 2016

Ms.R.Srilekha, PG Student published a International Journal of Advanced Engineering and Research Development (IJAERD) entitled "Disc type Transformer windings in an OD Cooling mode with Directed oil flow "Paper ID #: IJAERD2016V03I1145381 , Volume 03 Issue 11, November – 2016

B.Vijay, PG Student published a International Journal of Advanced Engineering and Research Development (IJAERD) entitled "Analysis the performance of 15KW SPV Plant AT PEC" Paper ID #: IJAERD2016V03I1145381 , Volume 03 Issue 11, November - 2016

DEPARTMENT ACHIEVEMENTS

Two days Workshop on “Real Time PCB Design and Fabrication Techniques” to the EEE students on 31.08.2016 and 01.09.2016 organized by MARZ INFOTECH.



Workshop on “Real Time PCB Design and Fabrication Techniques”